VO2223

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Optocoupler, Power Phototriac



PIN	FUNCTION				
1	LED cathode				
2	LED anode				
3	LED cathode				
4	LED cathode				
5	Triac gate				
6	Triac T1				
7	Triac T2				

DESCRIPTION

The VO2223 is an optically couple phototriac driving a power triac in a DIP-8 package. It provides a 5300 V of input to output isolation.

FEATURES

- Maximum trigger current (I_{FT}): 10 mA
- Isolation test voltage 5300 V_{RMS}
- Peak off-state voltage 600 V
- Load current 0.9 A_{RMS}
- dV/dt of 210 V/µs
- DIP-8 package
- Pure tin leads
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Home appliances (air conditioners, microwave ovens, washing machines, personal hygiene systems, refrigerators, fan heaters, inductive heating cooker, water heaters, etc.)
- Industrial equipments

AGENCY APPROVALS

- UL E52744 system code H
- cUL E52744 system code H
- VDE DIN EN 60747-5-5 (VDE 0884-5)

ORDERING INFORMATION					
VO222 PART NUMBER	3 - X 0 0 # DIP-# PACKAGE OPTION				
AGENCY CERTIFIED/PACKAGE	TRIGGER, CURRENT I _{FT} (mA)				
UL, cUL	10				
DIP-8	VO2223				
VDE, UL, cUL	10				
	VO2223-X001				

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
LED continuous forward current		I _F	50	mA			
LED reverse voltage		VR	6	V			
OUTPUT							
Repetitive peak off-state voltage	Sine wave, 50 Hz to 60 Hz, gate open	V _{DRM}	600	V			
On-state RMS current		I _{T(RMS)}	0.9	А			
Peak non-repetitive surge current (60 Hz, 1 cycle)		I _{TSM}	9	А			
COUPLER							
Total power dissipation ⁽²⁾		P _{diss}	1.2	W			
Ambient temperature range		T _{amb}	- 40 to + 85	°C			
Storage temperature range		T _{stg}	- 40 to + 125	°C			
Soldering temperature ⁽¹⁾	$t \le 10$ s max.	T _{sld}	260	°C			
Isolation test voltage	for 1 s	VISO	5300	V _{RMS}			

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to wave profile for soldering conditions for through hole devices.

⁽²⁾ Total power dissipation value is based on 2S2P PCB.

Rev. 1.3, 12-Sep-12

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COMPLIANT





ABSOLUTE MAXIMUM RATING CURVES





Fig. 2 - Allowable Load Current vs. Ambient Temperature

Note

 The allowable load current was calculated out under a given operating conditions and only for reference: LED power: Q_E = 0.015 W, θ_{BA} (4-layer) = 30 °C/W

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
LED trigger current	V _T = 6 V	I _{FT}			10	mA	
LED reverse current	V _R = 5 V	I _R			10	μA	
LED forward voltage	I _F = 10 mA	V _F	0.9		1.3	V	
OUTPUT							
Peak on-state voltage	$I_F = 10 \text{ mA}, I_{TM} = \text{max}.$	V _{TM}			2.5	V	
Peak off-state current	$I_{F} = 10 \text{ mA}, V_{DRM} = 600 \text{ V}$	I _{DRM}			100	μA	
Holding current	$R_L = 100 \ \Omega$	Ι _Η			25	mA	
Critical rate of rise of off-state voltage	$V_{IN} = 400 V_{RMS}$ (fig. 3)	. 3) dV/dt _{cr} 21		210		V/µs	
Critical rate of rise of commutating voltage	$\label{eq:VIN} \begin{array}{l} V_{IN} = 240 \ V_{RMS}, \\ I_T = 1 \ A_{RMS} \ (fig. \ 3) \end{array}$	dV/dt _{crq} 0.7			V/µs		

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.



Fig. 3 - dV/dt Test Circuit

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SAFETY AND INSULATION RATINGS							
PARAMETER		TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification		IEC 68 part 1			40/85/21		
Pollution degree		DIN VDE0109			2		
Tracking resistance (comparative tracking index)		Insulation group IIIa	CTI	175			
Highest allowable overvoltage		Transient overvoltage	V _{IOTM}	8000			V _{peak}
Maximum working insulation voltage		Recurring peak voltage	V _{IORM}	890			V _{peak}
Insulation resistance at 25 °C		V _{IO} = 500 V	R _{IS}			≥ 10 ¹²	Ω
Insulation resistance at T _S		V _{IO} = 500 V	R _{IS}			≥ 10 ⁹	Ω
Insulation resistance at 100 °C		V _{IO} = 500 V	R _{IS}			≥ 10 ¹¹	Ω
Partial discharge test voltage		Method b, V _{pd} = V _{IORM} x 1.6	V _{pd}			1424	V _{peak}
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T _{SI}			165	°C
	Input current		I _{SI}			150	mA
	Output power		P _{SO}			2000	mW
Minimum external air gap (clearance distance)		Measured from input terminals to output terminals, shortest distance through air		≥7			mm
Minimum external tracking (creepage distance)		Measured from input terminals to output terminals, shortest distance path along body		≥7			mm

Note

• This phototriac coupler is suitable for "Safe Electrical Insulation" only within the safety ratings. Compliance with safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



Fig. 4 - Forward Current vs. Forward Voltage



Fig. 5 - Reverse Voltage vs. Temperature

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Fig. 6 - On-State Current vs. On-State Voltage



Fig. 7 - Off-State Leakage Current vs. Voltage



Fig. 8 - Normalized Trigger Input Current vs. Temperature



Fig. 9 - Trigger Input Current vs. Turn-on Time



Fig. 10 - Normalized Holding Current vs. Temperature



Fig. 11 - Trigger Current vs. Trigger Pulse Width

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Fig. 12 - Trigger Current vs. VLOAD





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PACKAGE MARKING (example of VO2223-X001)



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