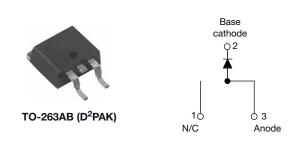
RoHS

Vishay Semiconductors

High Performance Schottky Rectifier, 20 A



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PRODUCT SUMMARY				
I _{F(AV)}	20 A			
V _R	15 V			
V _F at I _F	0.33 V			
I _{RM} max.	600 mA at 100 °C			
T _J max.	125 °C			
E _{AS}	10 mJ			
Package	TO-263AB (D ² PAK)			
Diode variation	Single die			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Single diode configuration
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
 COMPLIANT
 HALOGEN
 FREE
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	20	А			
V _{RRM}		15	V			
I _{FSM}	t _p = 5 μs sine	700	А			
V _F	19 A _{pk} , T _J = 125 °C (typical)	0.25	V			
TJ	Range	-55 to +125	۵°			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VS-20L15TSPbF	UNITS
Maximum DC reverse voltage	V _R	T.I = 100 °C	15	V
Maximum working peak reverse voltage	V _{RWM}	1 ₀ = 100 °C	15	v

ABSOLUTE MAXIMUM RATING	S				
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 85 °C,	rectangular waveform	20	
Maximum peak one cycle non-repetitive surge current	Isou	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	700	А
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	330	
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 \text{ °C}, I_{AS} = 2 \text{ A}, L = 6 \text{ mH}$		10	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maxim		2	А

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1



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ELECTRICAL SPECIFICA	TIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS
		19 A	T _{.1} = 25 °C	-	0.41	
Forward voltage drop	V _{FM} ⁽¹⁾	40 A	1j=25 C	-	0.52	v
See fig. 1	VFM (*)	19 A	T.I = 125 °C	0.25	0.33	v
		40 A	1j = 123 0	0.37	0.50	
Reverse leakage current	I _{RM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	$V_{\rm B} = Rated V_{\rm B}$	-	10	mA
See fig. 2	IRM W	T _J = 100 °C	VR - Haleu VR	-	600	ША
Threshold voltage	V _{F(TO)}	0.182		82	V	
Forward slope resistance	r _t	ij = ij maximum	$T_J = T_J maximum$			mΩ
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} , (test signal range 100 kHz to 1 MHz), 25 $^{\circ}\text{C}$		-	2000	pF
Typical series inductance	L _S	Measured lead to lead 5 m	m from package body	8	-	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs

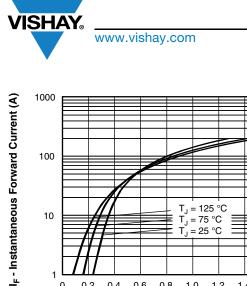
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	TJ		-55 to +125	°C
Maximum storage temperature range	T _{Stg}		-55 to +150	0
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	1.5	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (For TO-220)	0.50	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	40	
Approvimate weight			2	g
Approximate weight			0.07	oz.
minimum		Non-lubricated threads	6 (5)	kgf · cm
Mounting torque maximum			12 (10)	(lbf · in)
Marking device		Case style D ² PAK	20L1	5TS



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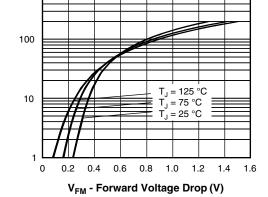


Fig. 1 - Maximum Forward Voltage Drop Characteristics

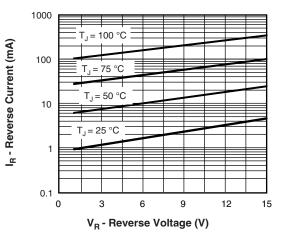


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

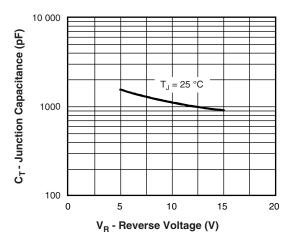


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

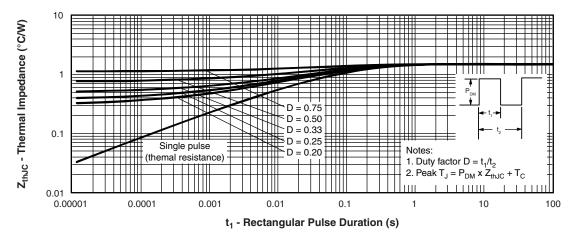
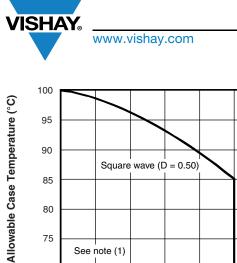


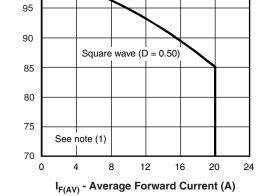
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

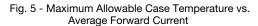
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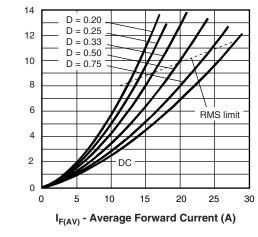


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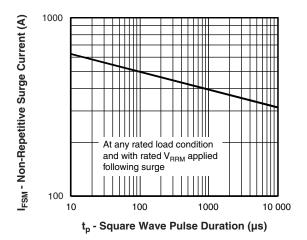












Average Power Loss (W)

Fig. 7 - Maximum Non-Repetitive Surge Current

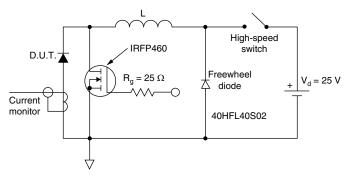


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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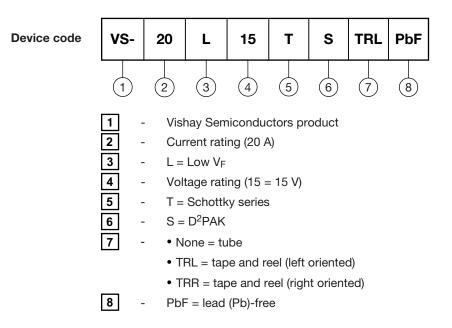
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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-20L15TSPbF	50	1000	Antistatic plastic tubes		
VS-20L15TSTRLPbF	800	800	13" diameter reel		
VS-20L15TSTRRPbF	800	800	13" diameter reel		
VS-20L15T-1PbF	50	1000	Antistatic plastic tubes		

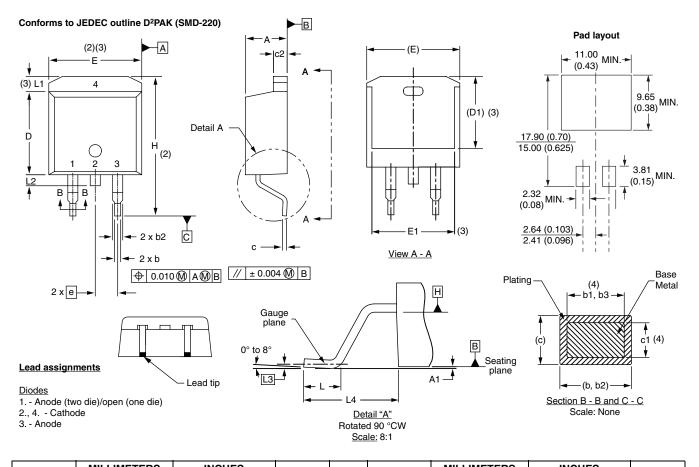
LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95014	
Part marking information	www.vishay.com/doc?95008	
Packaging information	www.vishay.com/doc?95032	

Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches

SHA



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMDOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

⁽⁷⁾ Outline conforms to JEDEC outline TO-263AB

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- ⁽⁴⁾ Dimension b1 and c1 apply to base metal only
- ⁽⁵⁾ Datum A and B to be determined at datum plane H
- ⁽⁶⁾ Controlling dimension: inch

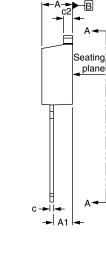
Vishay High Power Products

D²PAK, TO-262



DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



SYMBOL -	MILLIM	IETERS	INCH	INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- ⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

⁽⁶⁾ Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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